



WAFER TEST ENGINEER TECHNOLOGY DEVELOPMENT CENTRE GODMANCHESTER

CDT currently has a vacancy for a Wafer Test Engineer to setup testing equipment and test processes for testing Silicon Wafers. You will perform Known Good Die tests and undertake design verification and characterisation of OLED display driving circuits.

In this role, you will program and setup Wafer Probers and mixed signal test equipment, as well as supporting the Design team for new wafer designs. Through preparing and testing the Standard Operating Procedures (SOP), you will perform Risk Assessments (RA) for the testing of wafers and identify and implement process improvement for new and existing test processes. In addition, you will actively participate in the development of CDT's Intellectual Property portfolio.

Ideally educated to degree level or equivalent in Electronic Engineering, or a related discipline, with experience in semiconductor wafer testing, you will be able to use Wafer probe equipment and Semiconductor Parameter Analysers and ideally have an understanding of Silicon Wafer design and manufacturing processes. Direct experience in semiconductor testing or display testing and knowledge of Electroglas Probe Stations and Fairchild Sentry mixed signal tester would be highly advantageous. A strong knowledge of electronics is essential, as is strong ATE test experience and knowledge.

You must be an excellent communicator with the ability to work independently, using your initiative to solve technical problems. Practical, methodical and organised you should also be able to demonstrate a highly flexible approach and react quickly and appropriately to changing priorities and unexpected demands. A good team player, able to function effectively in a multi-disciplinary team environment, you should have outstanding attention to detail and a high level of computer literacy to include a working knowledge of Microsoft Office. Furthermore, you will have sound problem solving skills combined with Failure Analysis skills.

This is an outstanding opportunity to be part of a dynamic environment where your contribution will earn full recognition and reward. CDT offers a competitive salary and benefits package including 27 days holiday, private health and dental care, life assurance and pension scheme. Please apply with full CV to: The Human Resources Department, CDT Ltd, Building 2020, Cambourne Business Park, Cambridgeshire CB23 6DW. E-mail: careers@cdtltd.co.uk. Please quote the reference 'Wafer Test Engineer' in the subject heading.

A full job description is available on request.

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